

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A printhead integrated circuit comprising
a substrate; and
a plurality of nozzle arrangements that is positioned on the substrate, each nozzle arrangement comprising
a nozzle chamber structure that is positioned on the substrate and that defines a nozzle chamber having an ink ejection port from which ink is to be ejected;
an ink-ejecting mechanism that is operatively arranged with respect to the nozzle chamber structure, the ink-ejecting mechanism including at least one ~~moving component~~ piston that is displaceable to generate a pressure pulse within the nozzle chamber to eject ink from the ink ejection port;
an actuator that is positioned on the substrate and that has at least one working member that is of a material having a coefficient of thermal expansion such that the, or each, working member is capable of substantially rectilinear expansion and contraction relative to the substrate when heated and subsequently cooled; and
an energy transmitting means that interconnects the, or each, moving component and the, or each, working member so that energy generated by the, or each, working member as a result of expansion and subsequent contraction of the, or each, working member is transmitted to the, or each, ~~moving component~~ piston resulting in displacement of the, or each, ~~moving component~~ piston and generation of said pressure pulse,
wherein, in each nozzle arrangement, the ink ejection port is arranged in the nozzle chamber so as to be adjacent a distal end portion of the ~~moving component~~ piston with respect to the working member and energy transmitting means; ~~and~~
~~each actuator includes one elongate working arm that has a fixed end portion and a working end portion, the working arm being configured to define part of a resistive heating circuit.~~
2. (Cancelled)

3. (Previously Presented) A printhead integrated circuit as claimed in claim 1, in which the substrate includes a silicon wafer substrate and a CMOS drive circuitry layer positioned on the silicon wafer substrate.

4. (Cancelled)

5. (Currently Amended) A printhead integrated circuit as claimed in ~~claim 4~~claim 1, in which each nozzle chamber structure includes nozzle chamber walls and a roof positioned on the nozzle chamber walls, the roof defining the ink ejection port from which ink is ejected upon generation of said pressure pulse.

6. (Previously Presented) A printhead integrated circuit as claimed in claim 5, in which each ink-ejecting mechanism includes one moving component in the form of an ink-ejecting member that is positioned in the nozzle chamber and is displaceable towards and away from the roof to generate said pressure pulse.

7. (Withdrawn) A printhead integrated circuit as claimed in claim 6, in which the energy transmitting means is defined by the ink-ejecting member having an ink-ejecting surface area that is a predetermined order of magnitude larger than an opening area of the ink ejection port so that a hydraulic advantage is achieved.

8. (Withdrawn) A printhead integrated circuit as claimed in claim 6, in which the energy transmitting means is in the form of a motion amplification means that is configured so that movement of the ink-ejecting member is a predetermined order of magnitude greater than that of the working end portion of the working member.

9. (Withdrawn) A printhead integrated circuit as claimed in claim 8, in which the motion amplification means includes a lever mechanism, the lever mechanism defining an effort arm that is connected to the working end portion of the working member and the ink-ejecting member defining a load arm, the load arm having an effective length that is a predetermined order of magnitude greater than an effective length of the effort arm, the lever mechanism further defining a fulcrum that connects the effort and load arms pivotally to one of the substrate and the nozzle chamber structure.

10. (Previously Presented) An inkjet printhead that includes at least one printhead integrated circuit as claimed in claim 1.